



04-14-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit 2183

Examiner Eric Coleman

In Re: Mario Nemirovsky et al.
Case: P3817
Serial No.: 09/706,157
Filed: 11/03/2000
Subject: Clustering Stream and/or Instruction Queues for Multi-Streaming Processors

To: Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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Dear Sir,

Information Disclosure Statement Under 37 C.F.R. 1.97

Enclosed herewith is an Information Disclosure Statement (IDS) by Applicant listing two (2) references. Applicant desires that the references be entered in the above referenced case as relevance dictates.

As prosecution has begun in this case, in accordance with rule 1.97 the required fee of \$180.00 enclosed. Please charge any deficiency to Deposit Account No. 50-0534.

Respectfully submitted,

Mario Nemirovsky et al.

by

Donald R. Boys

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Date of Deposit: 04/12/2004

Ref: Case Docket No.: P3817

First Named Inventor: Mario Nemirovsky

Serial Number: 09/706,157

Filing Date: 11/03/2000

Title of Case: Clustering Stream and/or Instruction Queues for Multi-Streaming Processors

I hereby certify that the attached papers are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and addressed to the Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

1. Information Disclosure Statement.
2. Cover Letter for Information Disclosure Statement.
3. Copies of two (2) publications cited in the IDS.
4. Certificate of express mailing.
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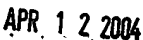
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Applicati n Number	09/706.157
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Filing Date	11/03/2000
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First Named Inventor	Mario Nemirovsky
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Art Unit	2183
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Examiner Name	Eric Coleman
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Attorney Docket Number	P3817
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Sheet	1	of	1
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
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DIEFENDORFF, KEITH, Power4 Focuses on Memory Bandwidth, Microprocessor Report, <http://www.mdronline.com/mpr/h/19991006/131302.html>, October 6, 1999

DIEFENDORFF, KEITH, Digital 21264 Sets New Standard, Microprocessor Report, <http://www.mdronline.com/mpr/h/19961028/101402.html>, October 28, 1996

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